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Circuit Diagram: Procedure: Apply input by using function generator to the circuit. Observe the output waveform on CRO. Measure the voltage at Output of first stage; Output of second stage. From the readings calculate voltage gain of first stage, second stage and overall gain of two stages.

RC Coupled Amplifier - Electronic Devices and Circuits Lab ...

electronic devices and circuits UNIT- V: junction transistor Biasing and Thermal Stabilization : would like for biasing, operational purpose, water line analysis, BJT biasing- ways, basic stability, fixed bias, collector to base bias, self bias, Stabilization against variations in VBE, Ic, and  $\beta$ , Stability factors, (S, S', S'' ), Bias compensation, Thermal runaway, Thermal stability.

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